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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09488942
	Filing Date		2000-01-21
	First Named Inventor	Paul W. SHERER	
	Art Unit	2152	
	Examiner Name	Sean M. REILLY	
	Attorney Docket Number	1112.US.CIP	

U.S. PATENTS						
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/	2	KANAKIA, HEMART and DAVID CHERITON, "The VMP Network Adapter Board (NAB): High-Performance Network Communication for Multiprocessors", ACM, 1988.	<input type="checkbox"/>
/	3	FUJITSU, LTD, Data sheet for "FUJITSU MB86950 Etherstar Ethernet Controller", December 1989.	<input type="checkbox"/>
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AS	5	FRAZIER, GREGORY L and YUVAL TARIAR, "The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches", IEEE, 1989.	<input type="checkbox"/>
	6	INTEL, CORP, Datasheet for 82596CA High-Performance 32-Bit Local Area Network Coprocessor", November 1989.	<input type="checkbox"/>
	7	NGAI, JOHN L and CHARLES L. SEITZ, "A Framework for Adaptive Routing in Multicomputer Networks", ACM, 1989.	<input type="checkbox"/>
	8	SCHRODER, MICHEAL D et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", PRINCIPALS OF COMPUTING SYSTEMS, April 21, 1990.	<input type="checkbox"/>
	9	GANSSE, JACK G., "Speed Kills", EMBEDDED SYSTEMS PROGRAMMING, August 1990.	<input type="checkbox"/>
	10	NATIONAL SEMICONDUCTOR, Data sheet for DP8390C/NS32490C chip, "Data Communications Local Area Network UARTS", 1990.	<input type="checkbox"/>
	11	NATIONAL SEMICONDUCTOR, Data sheet for "DP 83265 BSI Device (FDDI System Interface)", February 1991.	<input type="checkbox"/>
	12	KANAKIA, HR, Thesis, "High Performance Host Interfacing for Packet-Switched Networks", Stanford University Department of Computer Science, July 1991.	<input type="checkbox"/>
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<i>AK</i>	16	MOTOROLA, "FDDI System Interface Users Manual, MC68839", 1994.	<input type="checkbox"/>
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